

XIV JORNADA DE JÓVENES INVESTIGADORES/AS DEL I3A

# TSN Incremental Routing and Scheduling through parallel MILP computation

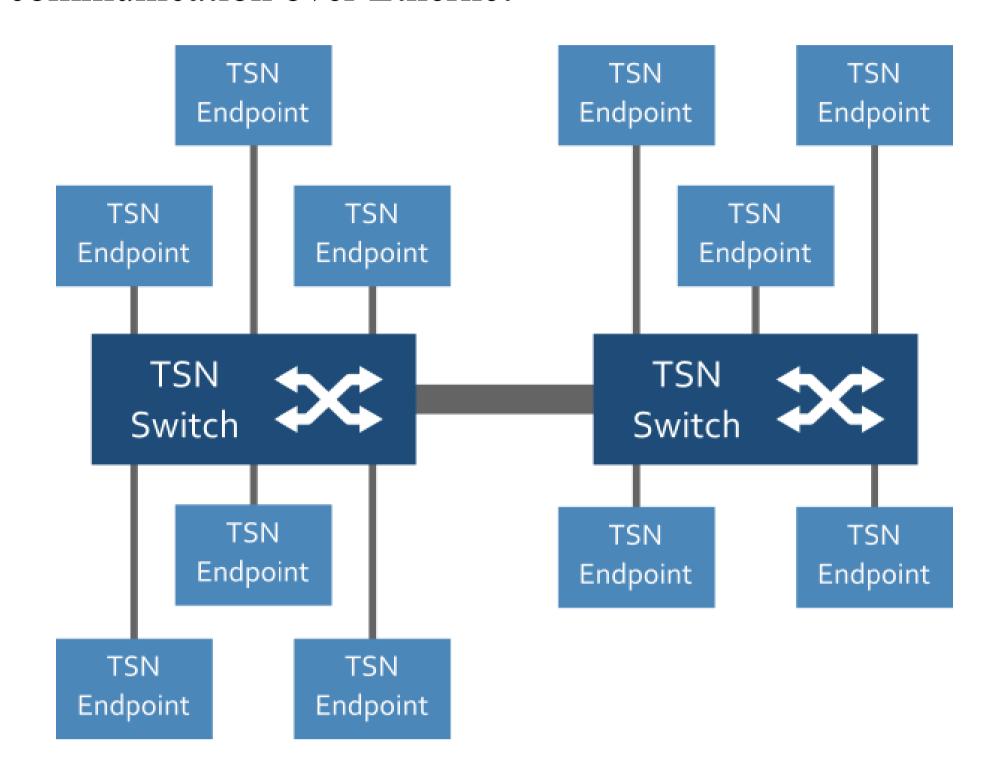
Álex Gracia<sup>1</sup>, Juan Segarra<sup>1</sup>, José Luis Briz<sup>1</sup>, Alitzel Torres<sup>1,2</sup>, Antonio Ramírez<sup>2</sup>, Héctor Blanco<sup>3</sup>



intel

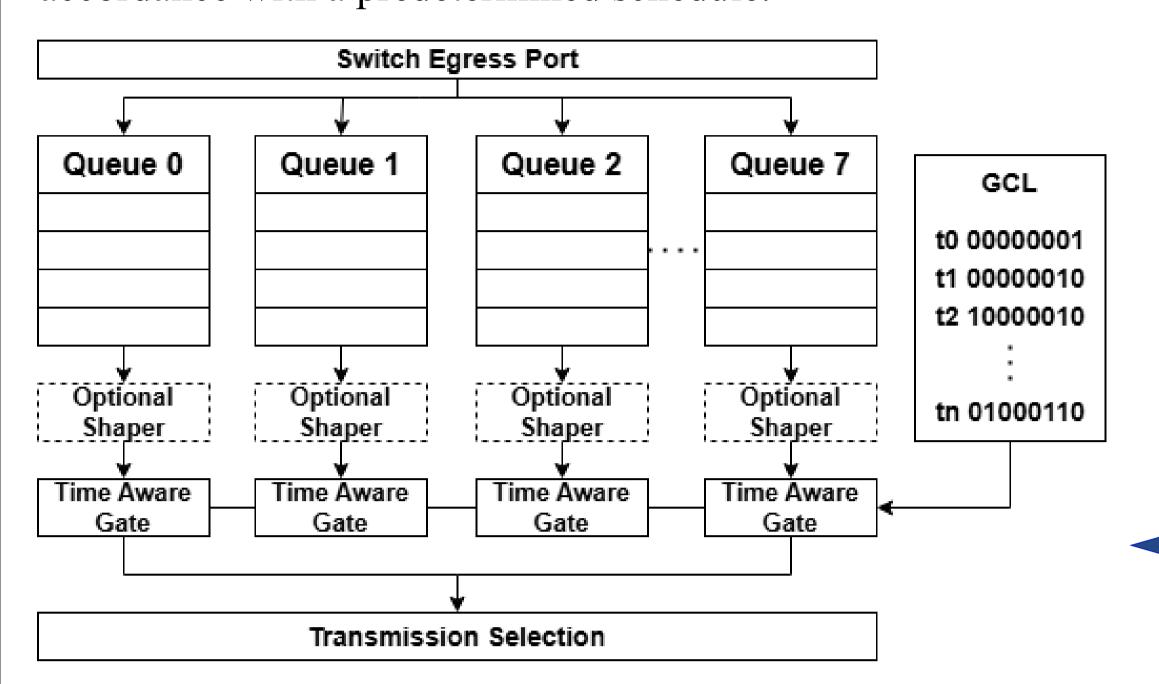
#### Time Sensitive Networking

TSN is a set of IEEE standards that enables deterministic communication over Ethernet



## IEEE 802.1 QBV - Time Aware Shaper

The IEEE 802.1Qbv standard delineates a time-triggered communication paradigm, incorporating a time-aware shaper (TAS) that governs the selection of frames at egress queues of network bridges in accordance with a predetermined schedule.



Scheduling a stream in the TSN network means optimizing the timing of its frames for the best path, and setting Gate Control List (GCL) accordingly

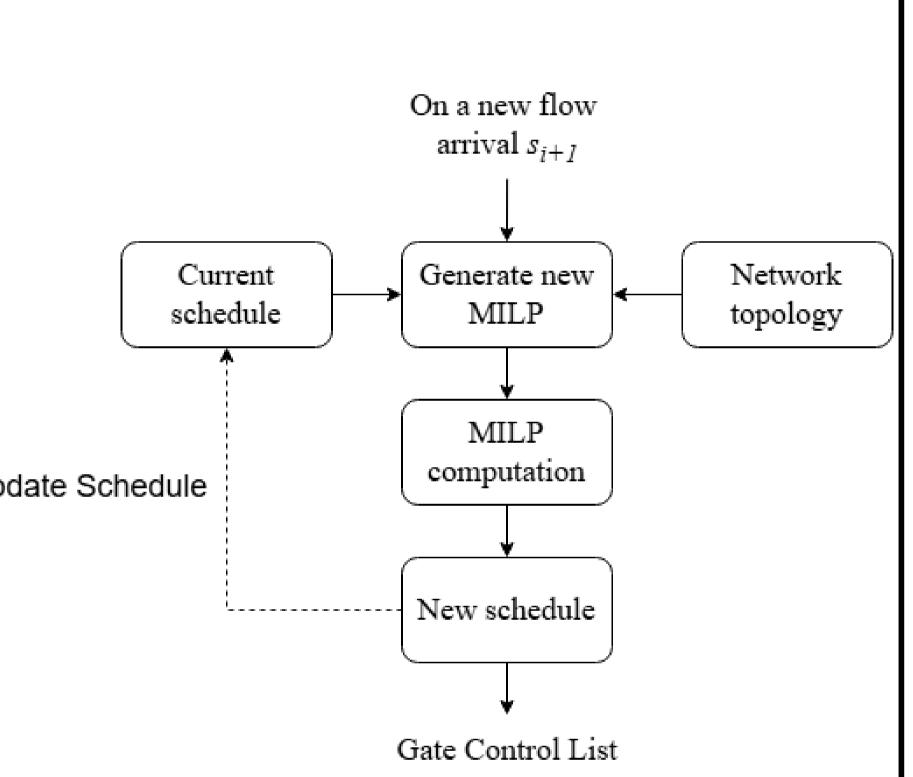
GCL SCHEDULE IS A NP
COMPLETE PROBLEM WITH
HIGH COMPUTATIONAL
DEMANDS!!!

#### Monolithic MILP Solution

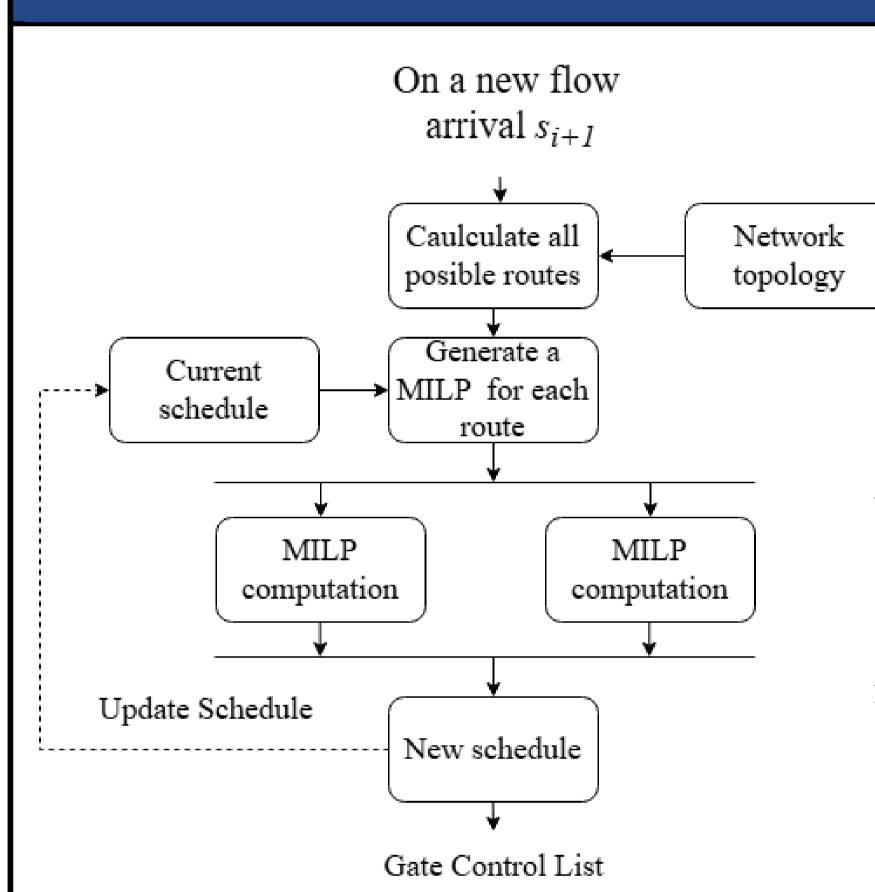
A MILP model based on [1] is constructed to schedule all frames of a single stream across all possible routes, and then selects the best schedule among the candidate paths.

Once a stream has been scheduled, a new model is generated for the subsequent Update Schedule streams, taking into account the

remaining network resources.

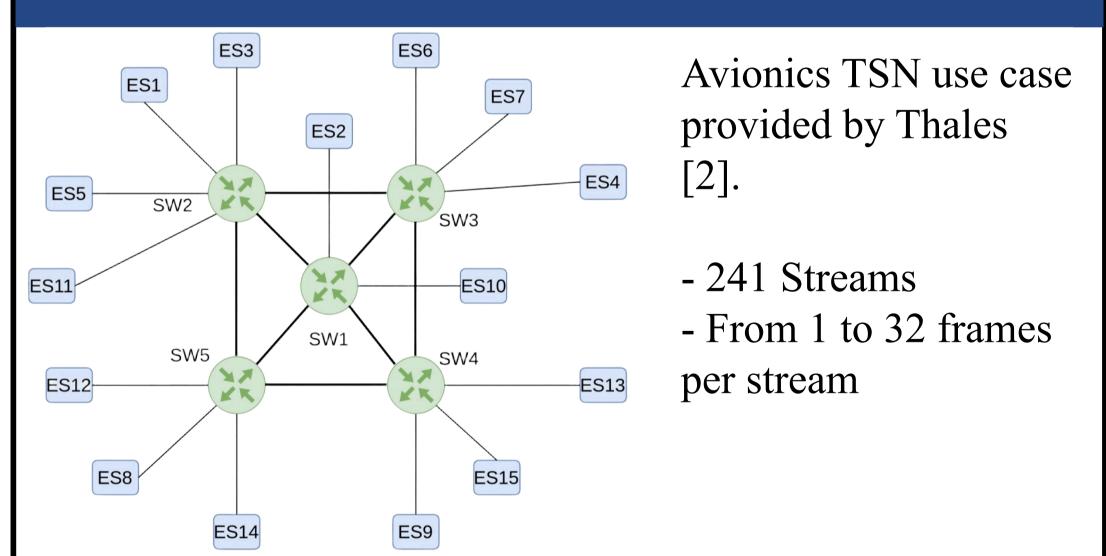


#### Parallel MILP Solution

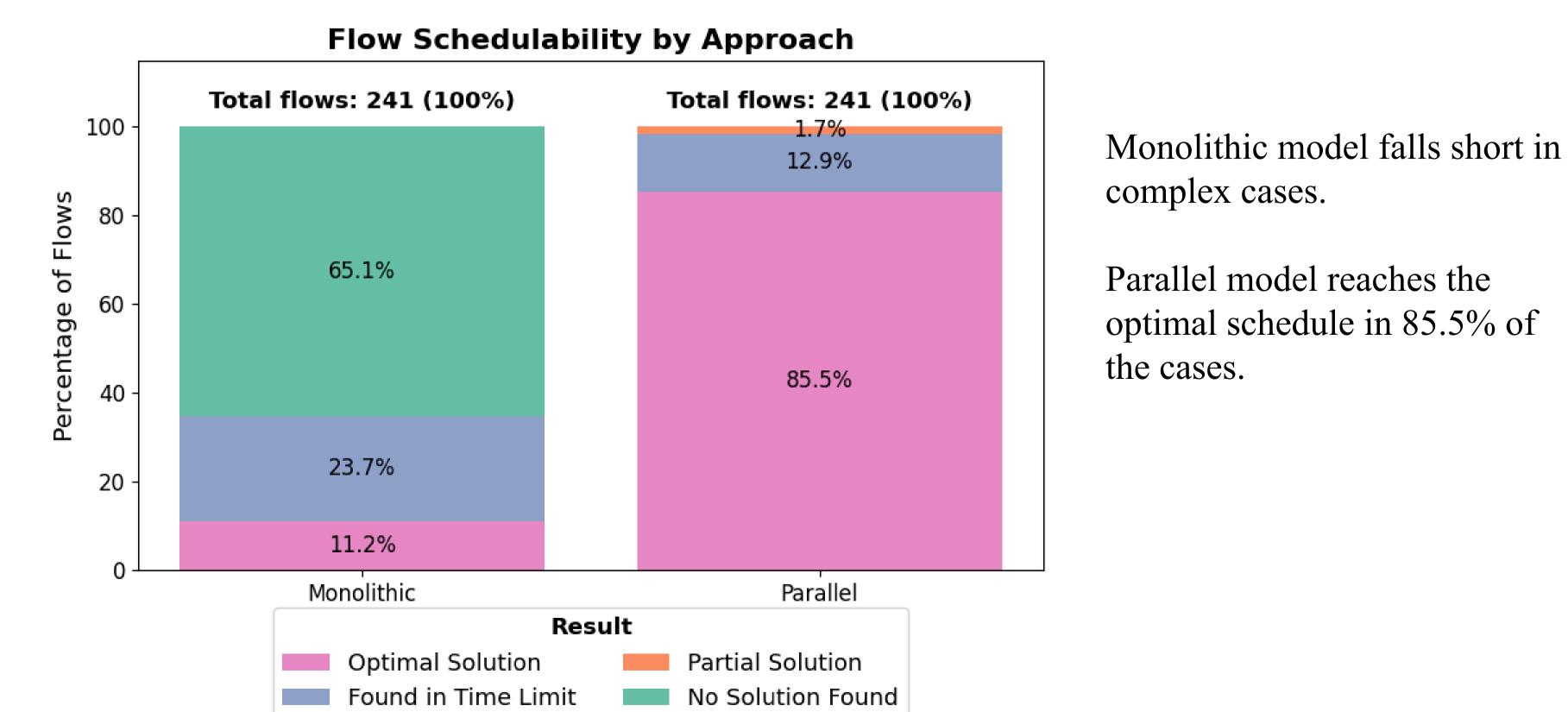


A MILP model is generated from all the candiadate paths and solved in parallel. The final path is selected based on the computed results.

#### TSN Avionics Use Case



**Evaluation Results** 



### **Evaluation Methodology**

- The experiments are conducted on an Intel® Xeon® Gold 5120 CPU running at 2.20 GHz, with 56 cores (Skylake architecture)
- MILP models are solved using the CBC solver [3], version 2.10.12.
- A one-hour timeout is set for solving the MILP model in the first approach. In the second approach, this time is evenly divided among the individual tasks.
- [1] A.G. Torres-Macías et al. Optimal and Fast IEEE 802.1Qbv Incremental Scheduling. Under Review, 2025. [2] Marc Boyer and Rafik Henia. Industrial challenge: Embedded reconfiguration of TSN. working paper or
- preprint, July 2024.
  [3] COIN-OR. CBC: COIN-OR Branch and Cut (Version 2.10.12) [software]. August 2024. DOI: 10.5281/zenodo.13347261
- 1. Dept. of Computing and Systems Engineering I3A, Universidad de Zaragoza
- 2. CINVESTAV Unidad Guadalajara, México
- 3.Intel Corporation Intel Deutschland GmbH